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nios ii is a 32 bit embedded processor architecture designed specifically for the altera family of field programmable gate array fpga integrated circuits nios ii incorporates many enhancements over the original nios architecture making it more suitable for a wider range of embedded computing applications from digital signal processing dsp the chapters in this handbook describe the nios ii processor architecture the programming model and the instruction set the nios ii processor is only availabe in the intel quartus prime 14 1 release and above this handbook assumes you have a basic familiarity with embedded processor concepts nios ii processors the nios ii processors are versatile and deliver unprecedented flexibility for your cost sensitive real time safety critical do 254 and applications processing needs learn more intel s nios ii is a soft processor defined in a hardware description language which can be implemented in intel s fpga devices by using the quartus prime cad system this tutorial provides a basic introduction to the nios ii processor intended for a user who wishes to implement a nios ii based system on an intel development and education board 1 overview of nios ii embedded development x 1 1 prerequisites for understanding the nios ii embedded design suite1 2 finding nios ii eds files1 3 nios ii software development environment1 4 nios ii eds development flows1 5 nios ii programs1 6 intel fpga software packages for embedded systems1 7 nios ii embedded design examples1 8 98 20k views 5 years ago engineer to engineer how to videos this video demonstrates the initial steps required to create a nios ii system from hardware and software perspectives the video nios ii implementations there are two flavors of the nios ii design one called nios ii e and the other called nios ii f the extensions stand for economical and fast respectively nios ii e is a slower but smaller core used where the microcontroller capability of the nios is most important nios ii processor reference guide attention the processor is part of a product obsolescence and support discontinuation schedule for the schedule refer to the product discontinuation notice about this user guide this getting started user guide provides initial information about how to use the altera nios ii development kit including unpacking the kit installing required software connecting the nios development board to a pc and running sample software nios ii software developer handbook updated for intel quartus prime design suite 21 3 attention the nios ii processor is part of a product obsolescence and support discontinuation schedule the intel nios ii processor is a 32 bit cpu that can be implemented in an intel fpga device two versions of the nios ii processor are available designated economy e and fast f the de1 soc computer includes two instances of the nios ii f version configured with floating point hardware support the intel nios ii processor is a 32 bit cpu that can be implemented in an intel fpga device two versions of the nios ii processor are available designated economy e and fast f the de10 nano computer includes two instances of the nios ii f version configured with floating point hardware support the nios v processor achieves 0 462 dmips mhz versus the nios ii processor s 0 107 dmips mhz and achieves an fmax of 566 mhz on intel agilex fpgas versus 400 mhz for nios ii processor the current stance is to stick with nios ii for now once altera release the new materials for nios v then it would be up to the individual professors to decide whether to continue using nios ii or move to nios v however do note that in near future altera will only be supporting nios v in the university program thanks regards kian embedded sopc design with nios ii processor and verilog

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