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interfaces effectively accelerate time to market of your gigabit digital designs high speed digital standards are quickly evolving to keep pace with emerging technologies such as 5g internet of things iot artificial intelligence ai virtual reality vr and autonomous vehicles in particular we conduct an extensive overview of the signaling properties of high speed serial i o including jitter noise and bit error rate ber wealsopresent design for testability assisted dft assisted test methods for manufacturing test this document provides information for designing high speed serial interfaces hssi on the aurix family of 32 bit microcontrollers from infineon covering the tc26x tc27x and tc29x devices high speed interface circuits and systems design 2 2 course topics basic high speed data link concepts channel characterization performance metrics link budget and trade offs equalization signaling schemes transmitter circuits receiver circuits clocking synchronization and timing recovery power and clock distribution 53 0 engineering information and connections for the global community of engineers find engineering games videos jobs disciplines calculators and articles interlaken provides a high speed interface with efficiency reliability and scalability the design of the interlaken interface ensures low power consumption and cost per bit without compromising on performance reach or reliability multiple design challenges efficiency the high speed serial interface hssi is a differential ecl serial interface standard developed by cisco systems and t3plus networking primarily for use in wan router

connections it is capable of speeds up to 52 mbit s with cables up to 50 feet 15 m in length high speed digital interfaces verification debugging and compliance testing our test and measurement solutions for high speed digital interfaces a highly configurable compute intensive block with up to 32 multipliers for network based applications in the data center high speed data enters an fpga based processing node in two fundamental ways through pcie connections to a host processor and via high speed ethernet connections to other data center resources

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